

FIGURE 1. SCHEMATIC BLOCK DIAGRAM OF A VERTICAL SPATIAL FILTERING CIRCUIT. (RELATED ART)

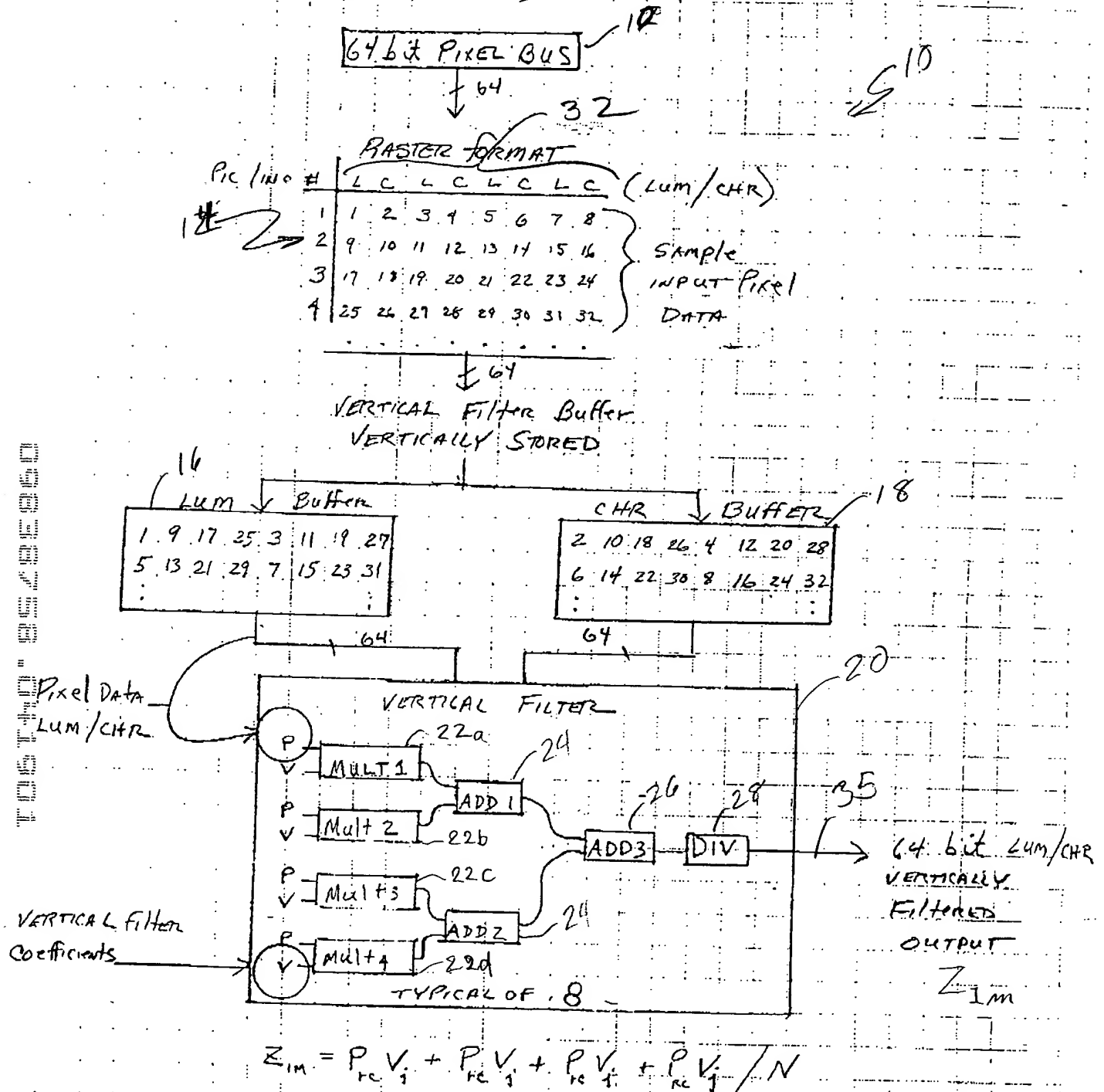
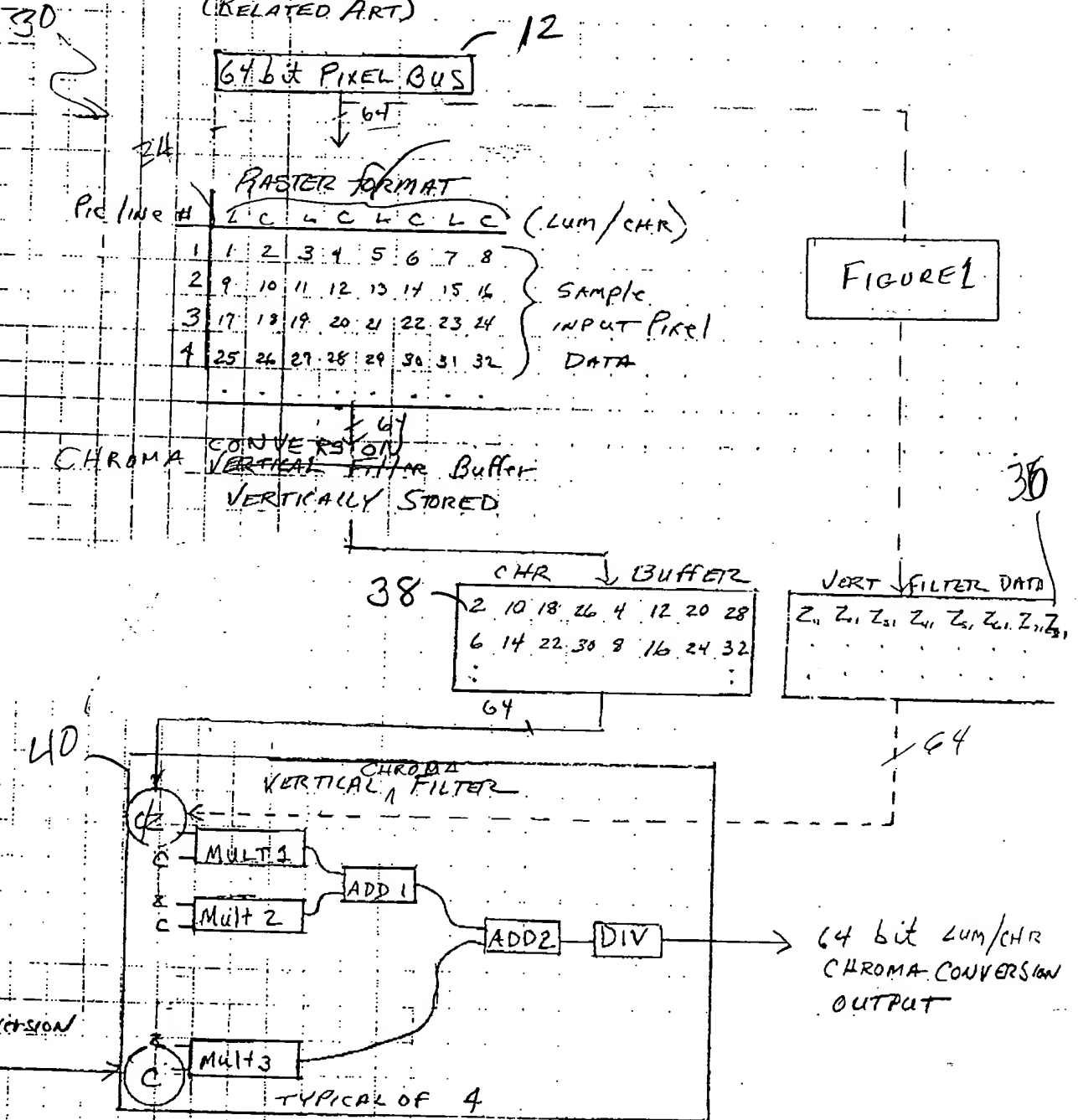
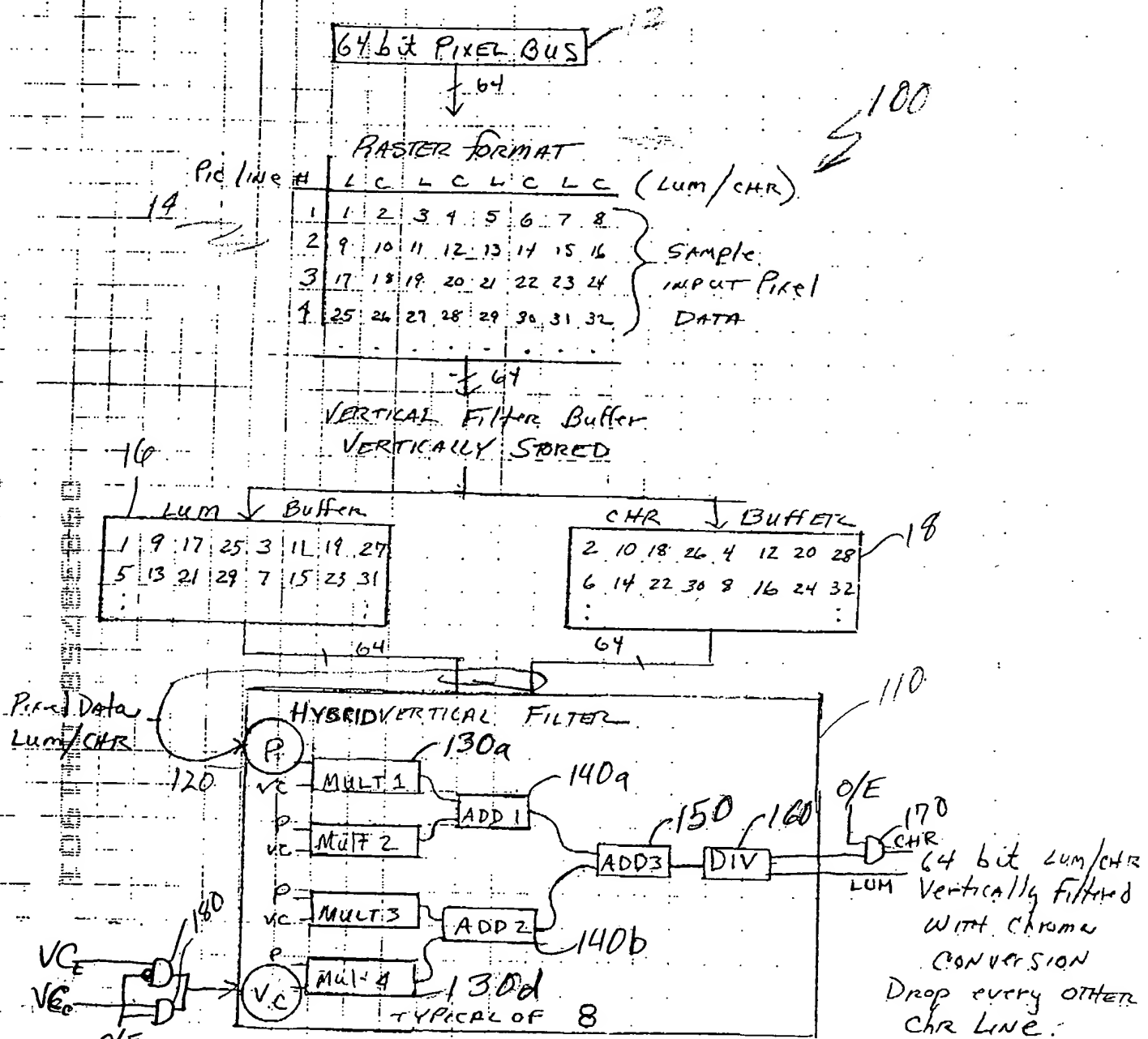


FIGURE 2 SCHEMATIC BLOCK DIAGRAM OF A VERTICAL CHROMA CONVERSION CIRCUIT (RELATED ART)



$$X_{no} = Z_{im} C_k + Z_{im} C_k + Z_{im} C_k$$

FIGURE 3 SCHEMATIC BLOCK DIAGRAM OF A VERTICAL FILTER/CHROMA CONVERSION CIRCUIT



$$I_{HD} = P_{RC} V_C + P(V_C + V_C) + P(V_C + V_C + V_C) + P(V_C + V_C + V_C) + P(V_C + V_C) + P V_C$$